

7. [pdf] [Techniques for Processing TCP/IP Flow Content in Network Switches ...](#)

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Content Addressable Memory, CISCO, Complex Instruction Set Computer, CLB, Configurable Logic Block, CPU, **Central Processing Unit ...**

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The calculating device is a **central unit** associated with a **memory**, wherein the first sub-group preferably includes **arithmetic** and/or **logic operations**, ...

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9. [Ladas & Parry LLP Patents](#)

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logic fits within two levels of **logic**, allowing the **unit** to operate at the, **Ternary Content Addressable Memory** (TCAM), **Content Addressable Memory ...**

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11. [WASP: A WSI Associative String Processor](#)

bit-parallel vector-vector **arithmetic**, **logic** and: relational **operations**, in active APes, for **ternary operations** such as, Z=XopY ...

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content addressable memory for Internet Protocol characterization", Field, o construct a pattern **matching unit** for a string, subsequently ...

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as **Ternary Content Addressable Memory** (TCAM), or, classification processors to the SRAM, **operations** provided by the **Arithmetic Logic Unit** (ALU), ...

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made up of processors containing a few registers, an **arithmetic unit**, The image **processing operations** in phase one are implemented via a ...

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accuracy **within one bit** of the encoder resolution (10'). The FPGA based encoder **processing unit** performs preprocessing, filtering and post pro- ...

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read from **memory**. Then, the **arithmetic operations** can be performed. Last, 41 shows a minimal computing system with a **central processing unit** ...

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33. [Telecommunications Glossary 2000](#)

memory: 1. All of the **addressable** storage space in a **processing unit** and other

One or more transmitters that are capable of **operation** while in motion. ...

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34. [exclamation mark " double quote % percent , comma 0 zero 0/1 ...](#)

(2001-01-27) **Arithmetic and Logic Unit** processor (ALU or "mill") The part of the

central processing unit which performs **operations** such as addition, ...

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35. [Projects by Community](#)

Uses BigDecimal to handle basic **arithmetic** and **ternary** expressions. ... Libraries and programs to support **unit** and functional testing of Java Uls. ...

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If **more than one** file is compared using the same criteria by **matching**, **central processing unit** (CPU) and as the backbone of the computer, ...

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37. [Tuesday.html](#)

A description of **memory operation** and various design details will be described.

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38. [PhD Thesis Links](#)

In many practical situations, **one or more** desired signals need to be recovered

from **arithmetic operations**, tests (or branches) and **memory operations**. ...

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best practices for **unit testing**). The Spring team have found that the correct that **one can perform** some **operations** with ADO.NET **within** a consistent ...

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40. TECHNICAL REPORT ABSTRACTS DATABASE # # Original design: Markus ...

Second, it allows a section of text to have **more than one** topic at a time, in use in industry as well as a pedagogical **arithmetic and logic unit** ...

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sentential **logic**, but a weaker theory than **arithmetic**, set theory, Whether the event **processing** is performed in **one central unit** or in a set of ...

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42. [Learning C# 2005 | O'Reilly Media](#)

These are comprised of **one or more** keys (referred to as a chord), be able to **perform arithmetic** on instances of your fractions (add two fractions, ...

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43. [1996 NASA SBIR & STTR Abstract Archives](#)

Meals prepared from "scratch" can involve 50 to 75 or **more** **sepa-rate unit operations**. Most available small appliances are designed to **perform only one**, ...

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45. [Telecomms Terms](#)

They **perform** logical **operations** and do **arithmetic**. In computer programming, **logic** Processor: Sometimes called the CPU, or **central processing unit** ...

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46. [sultanhesari](#)

Can have **more than one** **central** idea. Can brainstorm in outline, then convert the making it possible to run applications from **within** the storage unit ...

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arithmetic logic unit execution pipeline that may include **one or more** ... Each of the accelerator **units** may be **configured to perform one or more** dedicated ...

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In addition, a label may be applied to **one or more** content items by a user or The

evaluating unit is configured such that, by using a predetermined ...

www.caterpillar.com/infocenter/technical/ archive/kajima_2005/lat_arch_062005/

official journal 66062018.pdf - Circular notes

49. BibTeX bibliography `ipardistcomp.bib`

There was **one** volume per year, with %%% **one** to six issues, through 1989. digital

--- Parallel Processing: content addressable memory: ...

www.math.utah.edu/outreach/boardscomp.html • www.gates.org

50. [PDF] ICECS 2004 The 11th IEEE International Conference on Electronics ...

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of **logic** elements and hundreds of embedded multipliers and **memory units**.

matching to address the challenges of designs at RT and higher abstraction ...

www.ee.bou.ac.uk/~eecs04/Conference_Guide_and_Abstracts_Book-3.pdf

• **Chlorophyll** is the green pigment in plants that captures light energy for photosynthesis.

51. DoD SBIR FY04.1 - SOLICITATION SELECTIONS w/ ABSTRACTS

In addition, **one or more** new, advanced algorithms for retrieving detailed for a

large integer processor (LIP) able to perform arithmetic operations, ...

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one or more compositions infinitely many times. SERA's implementation, the

operations that a unit may perform during execution affect its syn- ...

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by **one or more operation**. Figure 6.5 shows that the **operation** stretch k can be

machine would not be **configured** properly. Thus, **unit** testing is very ...

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This means **one more** pointer indirection for every index **operation** in the array,

One benefit of **unit** testing is that it helps you develop your code ...

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Control - 2000-2001

55. [PDF] Information Technology Security Evaluation Manual (ITSEM) Version 1.0

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The recommended method for doing this is to **perform one or more** visits to

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The data-parallel programming model has been **one** of the **more** successful ap-

time performance penalties, the compilation system must **perform within ...**

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Each quantity belongs to exactly **one** dimension. A **unit** is select and **perform operations** without violating the consistency ...

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The 32-bit register whose bits reflect the outcome of certain fixed- point **operations**.

Fixed-Point Unit. A logic block that executes integer **arithmetic** and ...

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There is a basic crypto **unit** which can be **configured** to be a HCU or a TCU is either 0 or 1) rather than **ternary logic** (where the output of the gate ...

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The aggregation can be a simple **arithmetic operation** or a **more** complex sector

graph model vertices are placed uniformly at random in the **unit** square. ...

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More file operations The File class also supports a number of other file **UNIT**

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components that together **perform one or more** functions is represented by a single uniquely **addressable unit** of information **within** the document; ...

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plicitly commit to **one or more** autonomously developed ontologies. This technique permits quence of **operations** are treated as a **unit** ...

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When you start writing **unit** tests, you try to make your classes **more unit** ... of six

units of study (Boolean Functions and Computer **Arithmetic Logic**; ...

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GRIS executes (and creates the processes for) **one or more** affected the unit of computation. It is a sequential flow of control **within** a single ...

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66. [Cover Pages: XML Articles and Papers, January - March 2002](#)

Of course, a message could include **one or more** documents. **Within** a near decomposable **unit**, it is important to reduce complexity in data sharing. ...

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to extract **content** and **perform** automated **processing** on the documents. are used to ensure that a sequence of **operations** are treated as a **Unit**. For ...

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between the main **memory** and the CPU (center **processing unit**). runtime database record checkpoint as **one** where **one or more** **matching** records were found ...

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by **one or more** AMPs in order to **perform** a task in response to a request parcel. Loosely defined, a query block is a **unit** for which the Optimizer ...

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each action occurring **within** a node takes **one time unit**. Blocks of FPGA logic cells will be allocated and **configured** to **perform** the functional ...

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and **one or more** actors of the system **unit**. The purpose of a use case is to

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Because **one** of the **processing units** performs a computation intensive job,
arithmetic-geometric mean difference in terms of certain **central moments**. ...
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